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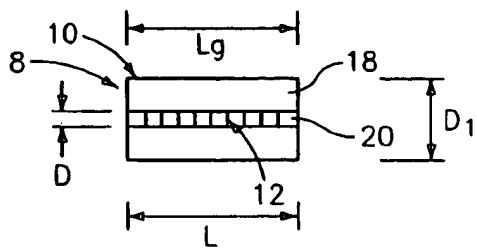


FIG. 1

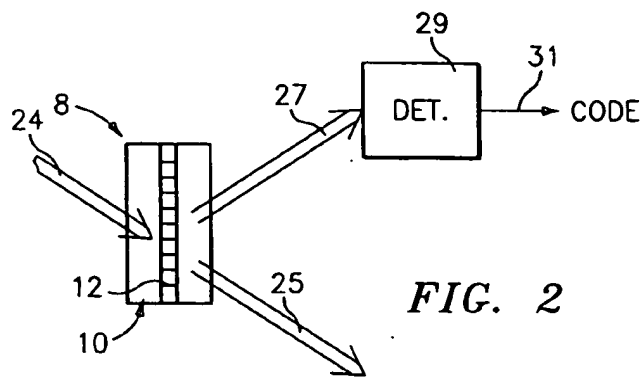


FIG. 2

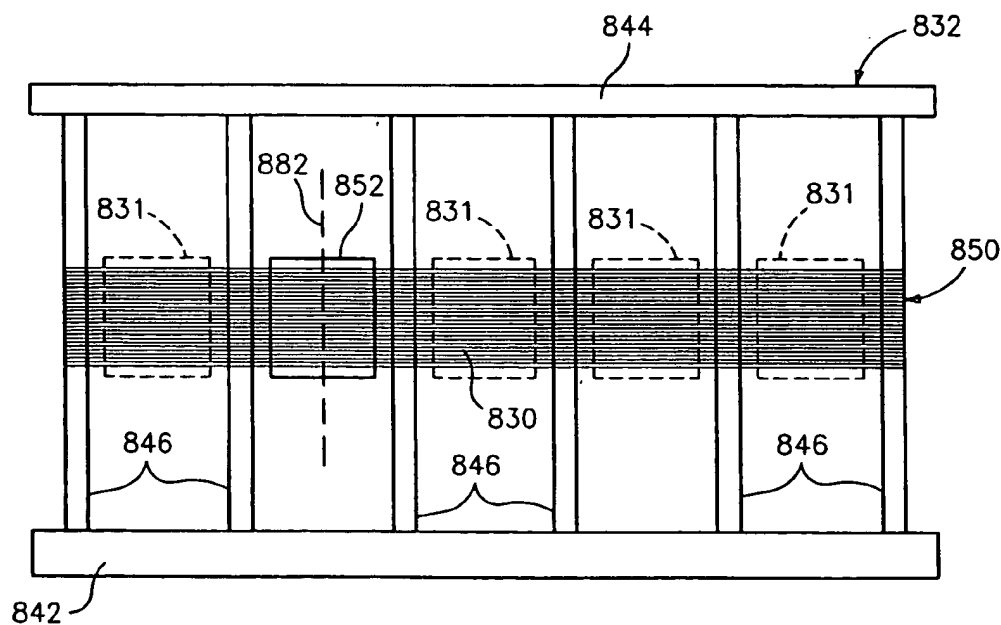


FIG. 6

Drawing Changes
Approved
10/4/04

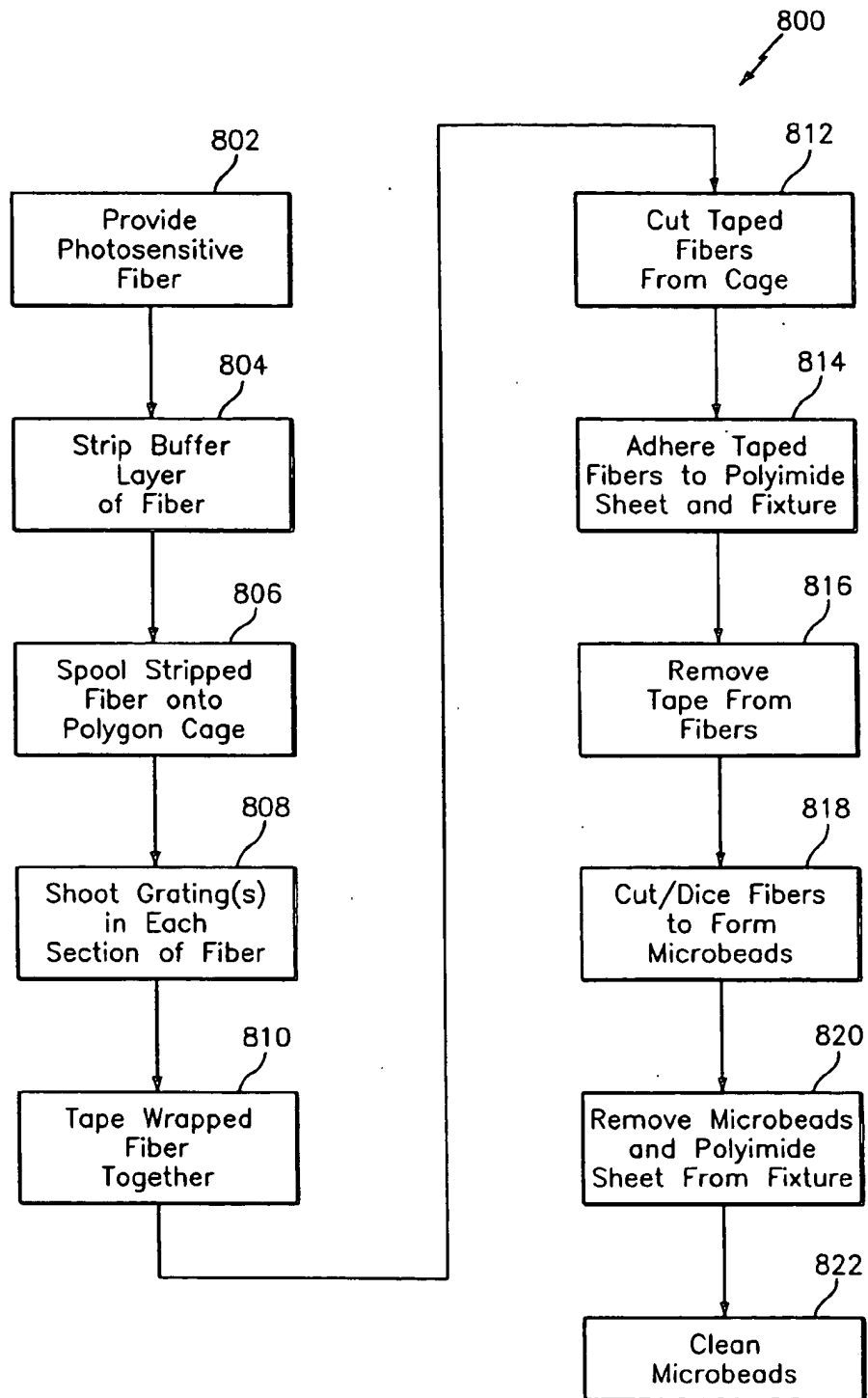


FIG. 3

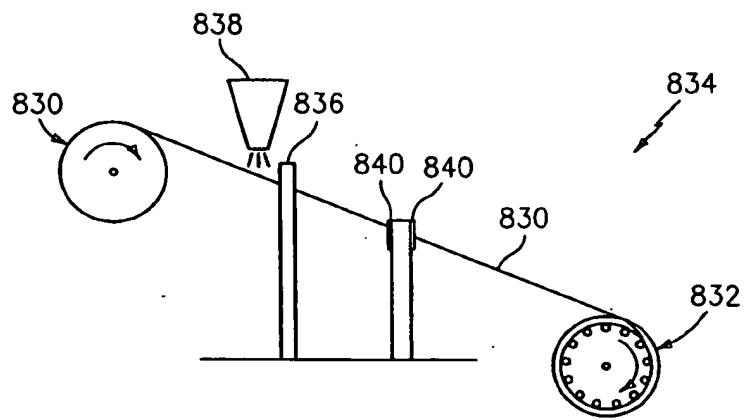


FIG. 4

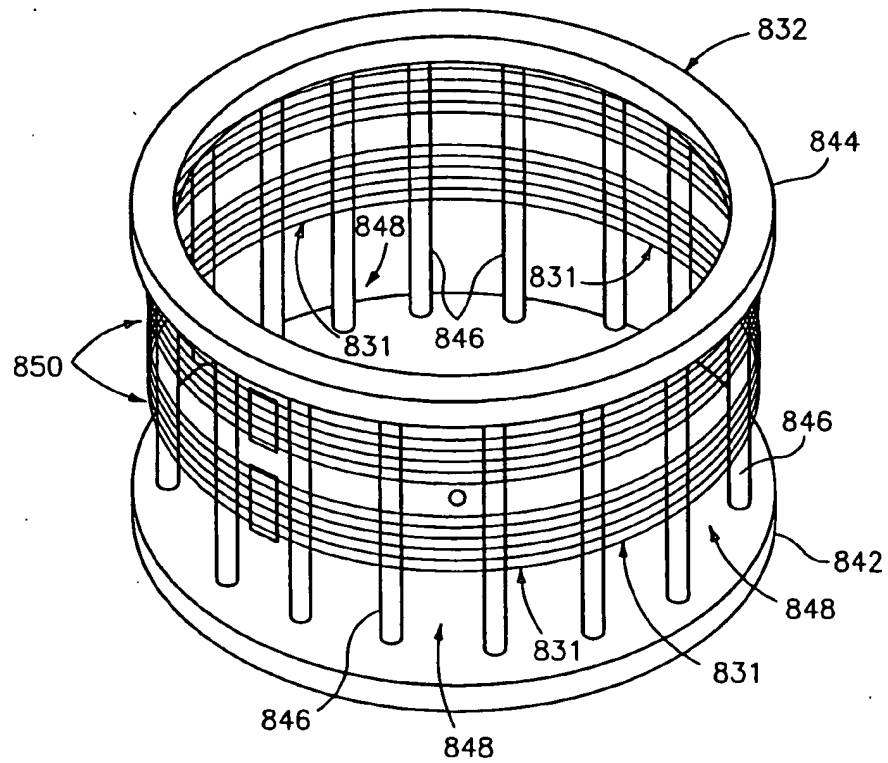


FIG. 5

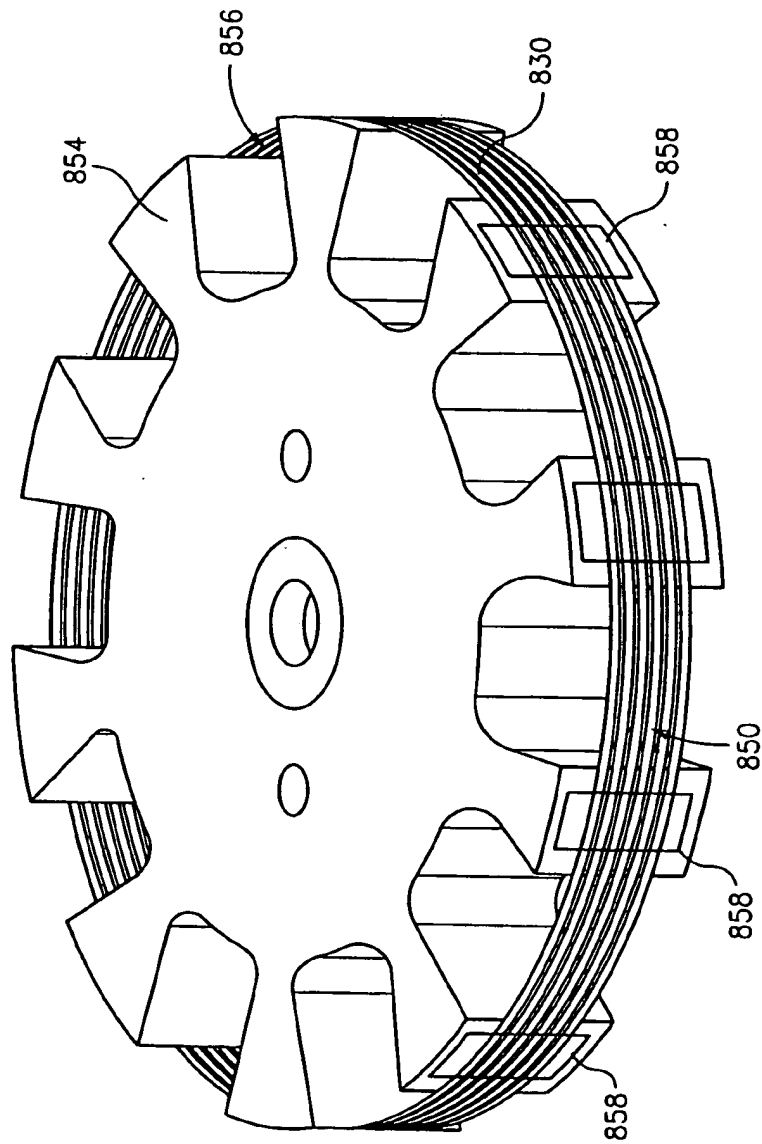


FIG. 7

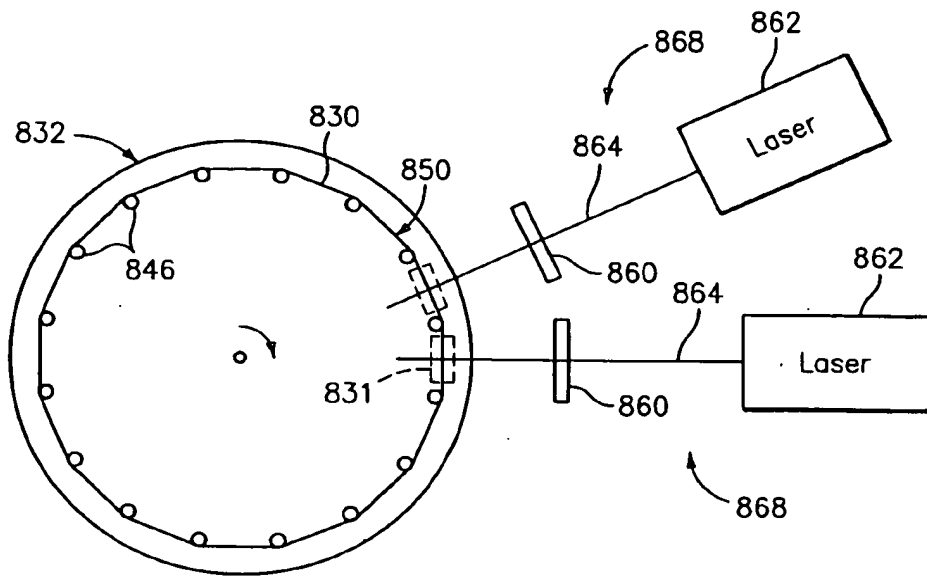


FIG. 8

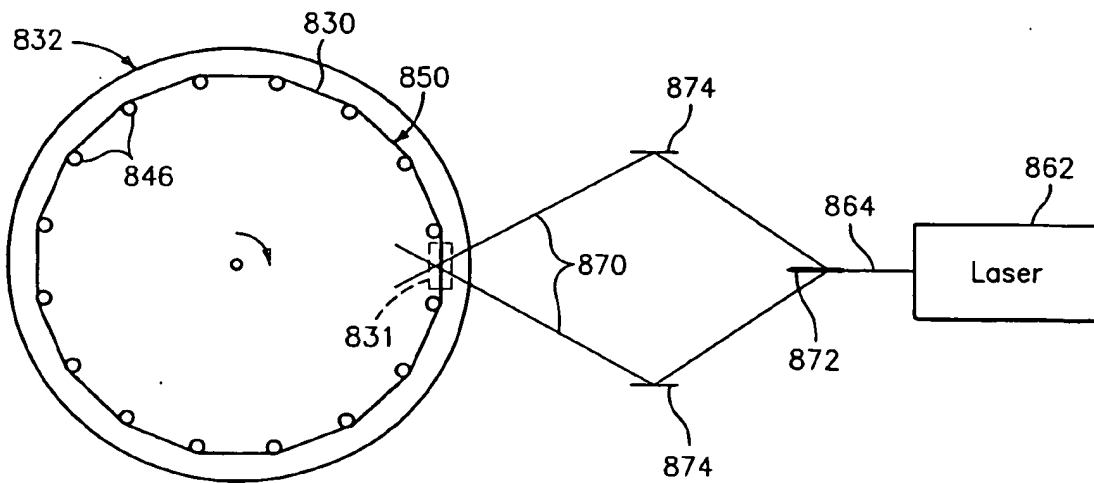


FIG. 9

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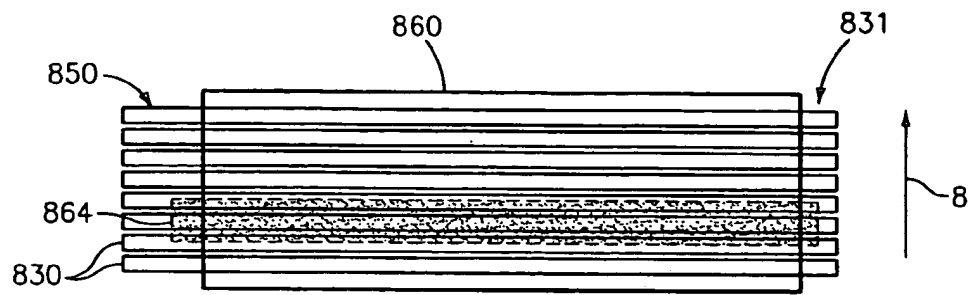


FIG. 10

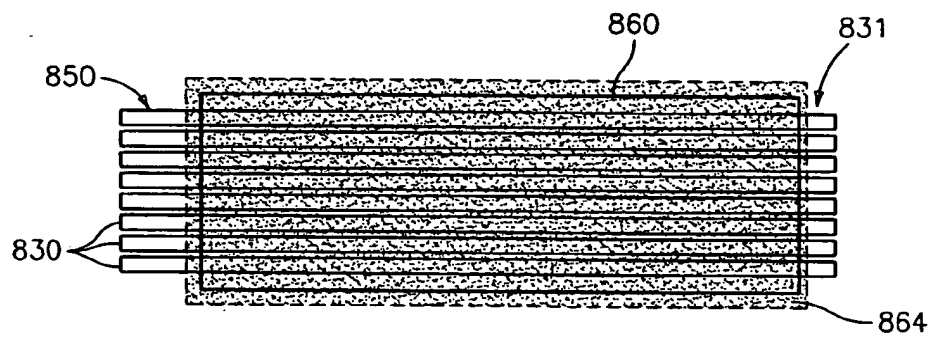


FIG. 11

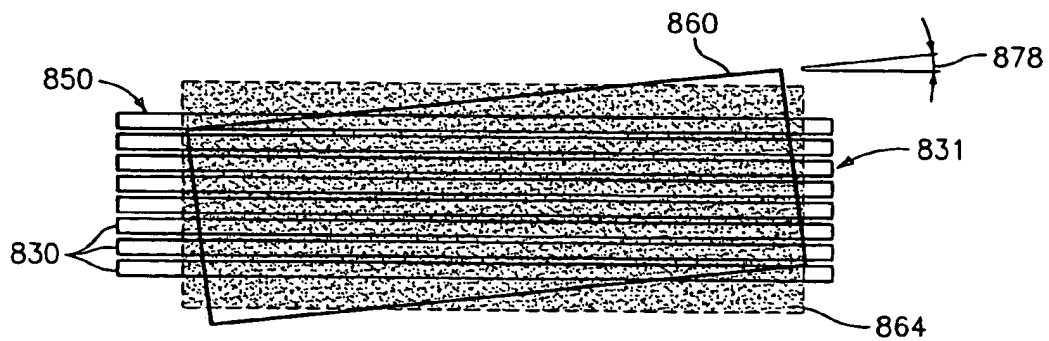


FIG. 12

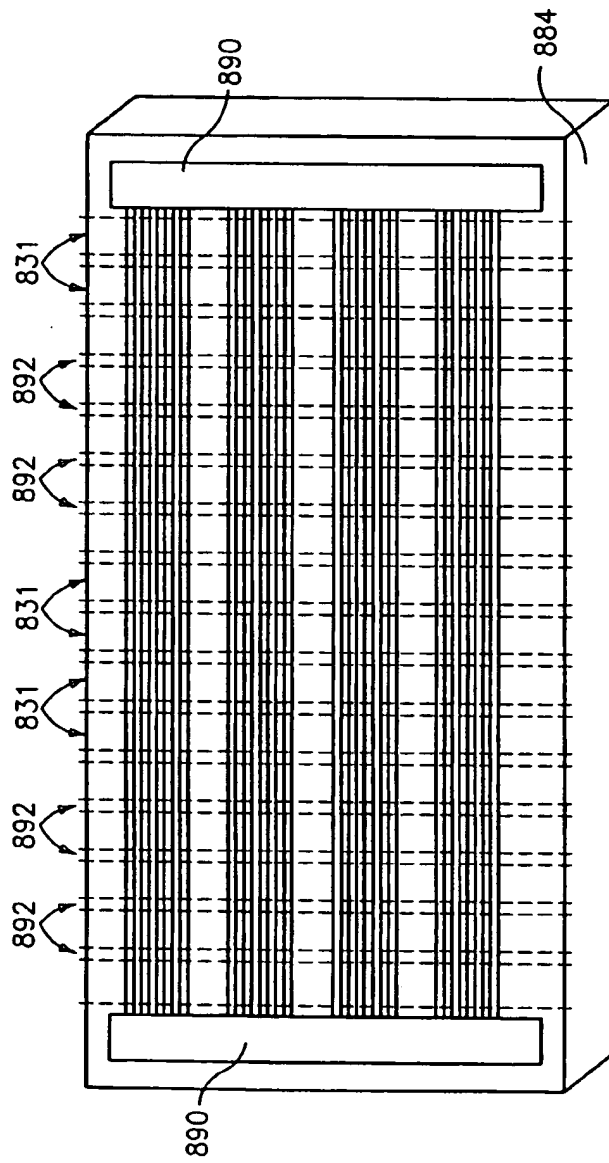


FIG. 13

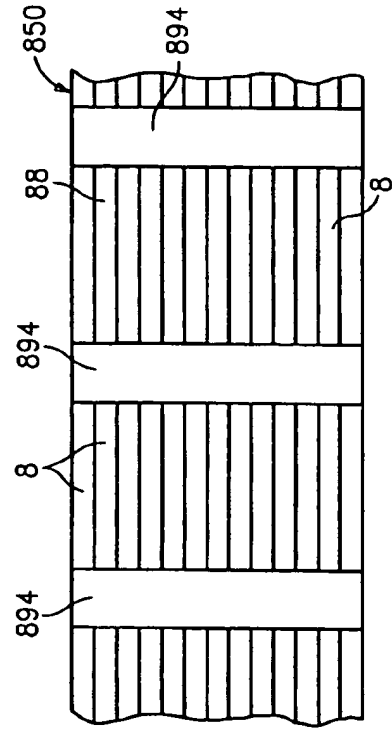


FIG. 15

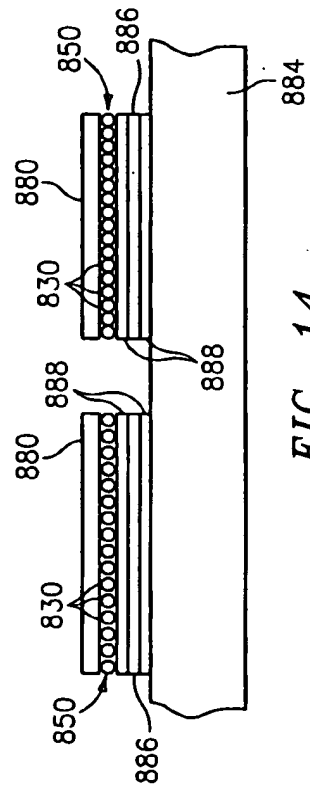


FIG. 14

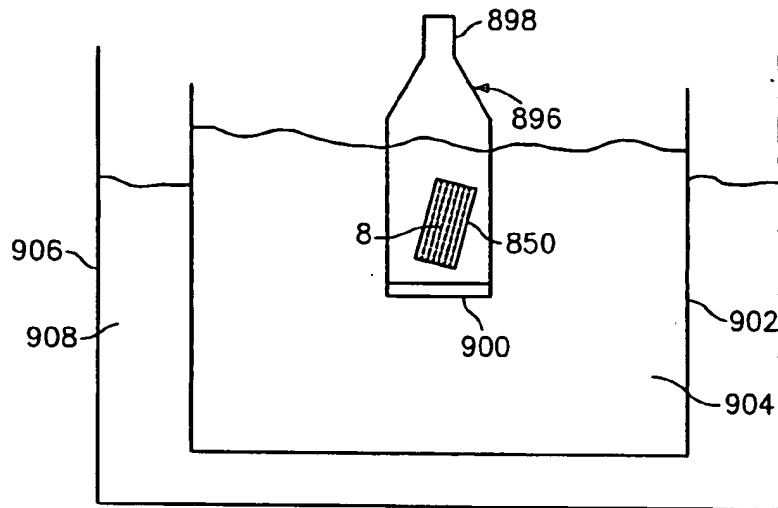


FIG. 16

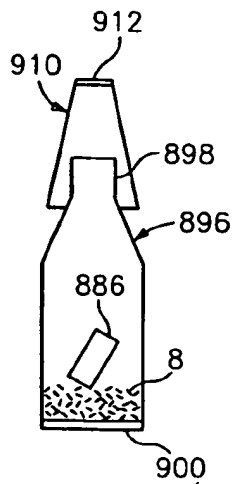


FIG. 17

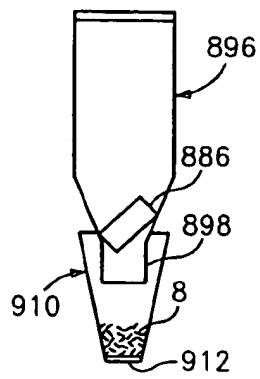


FIG. 18

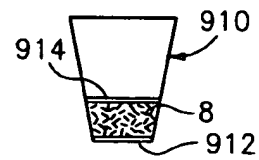


FIG. 19

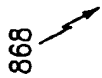


FIG. 20

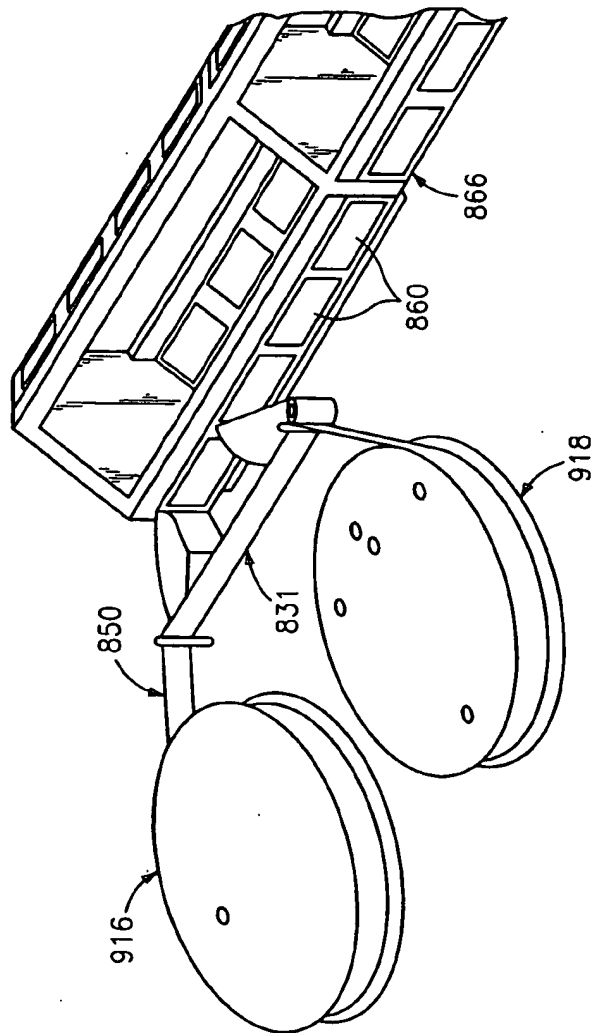


FIG. 21

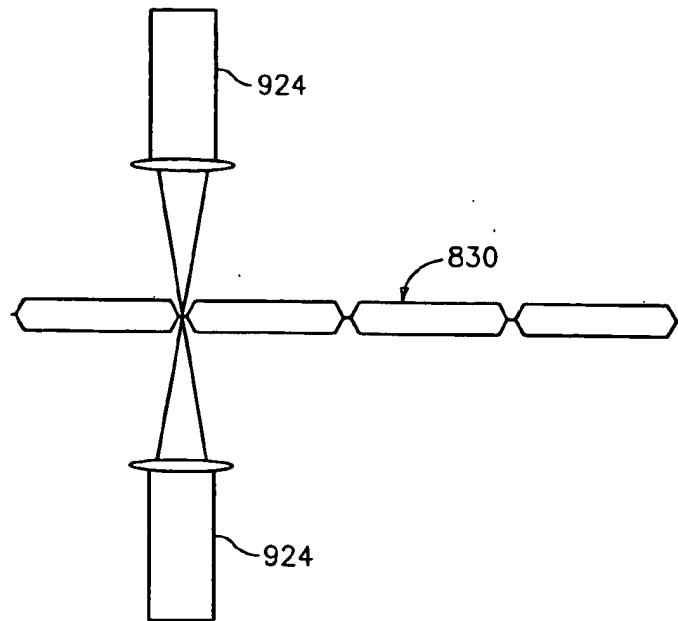


FIG. 22

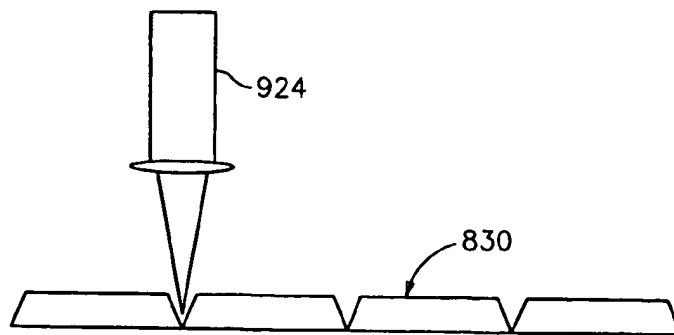


FIG. 23

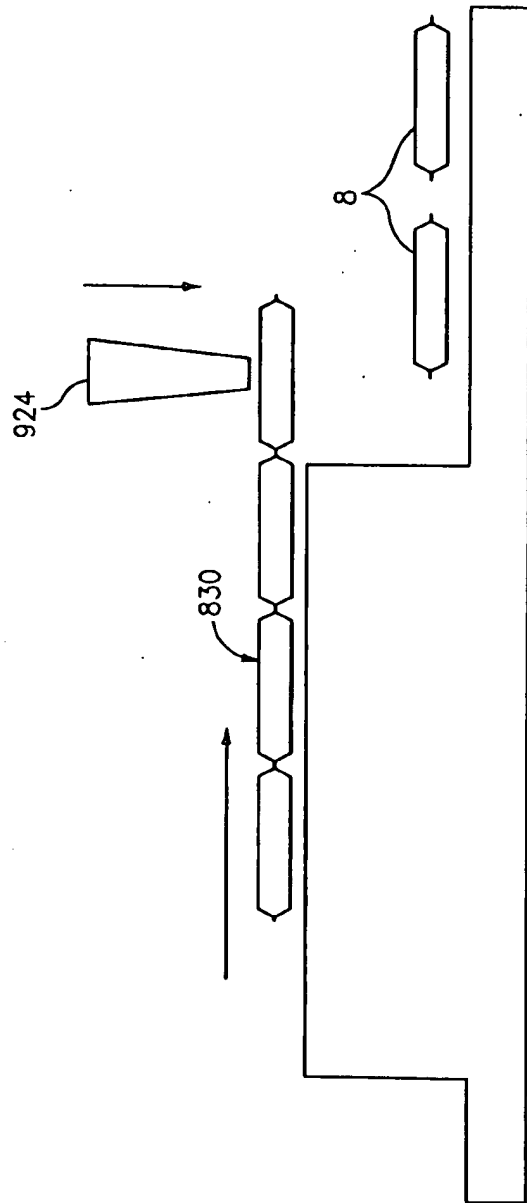


FIG. 24

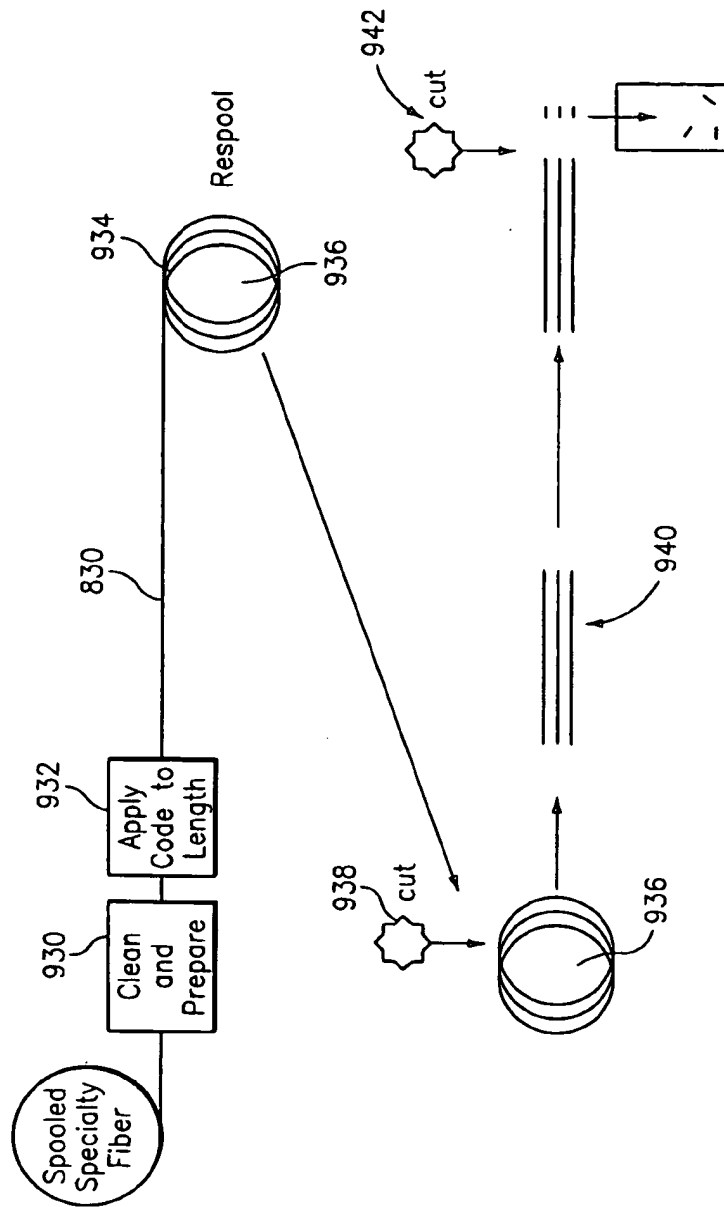


FIG. 25

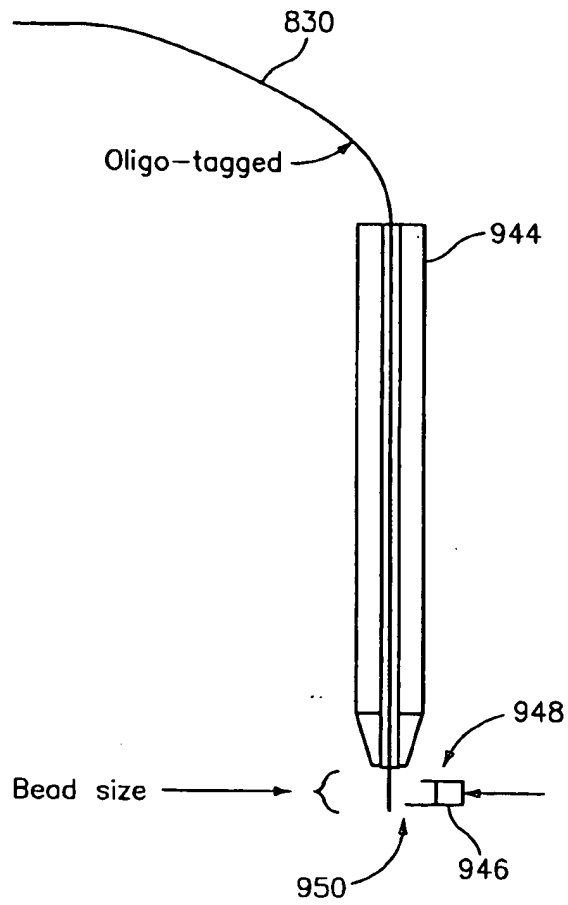


FIG. 26

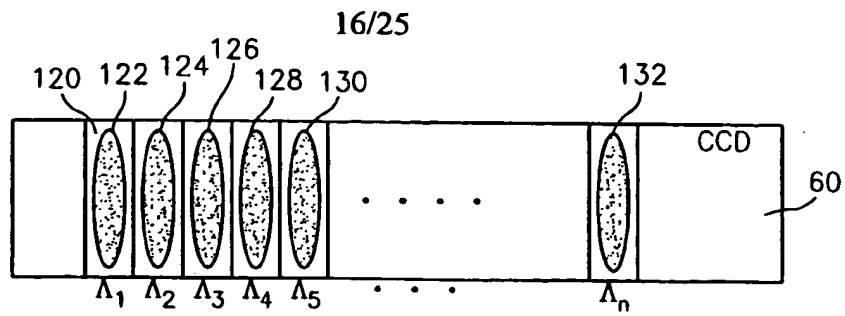


FIG. 28

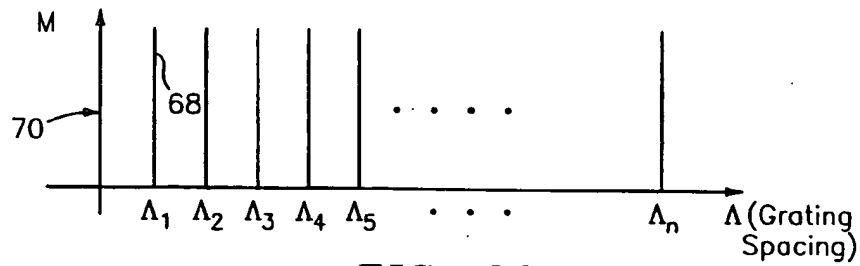
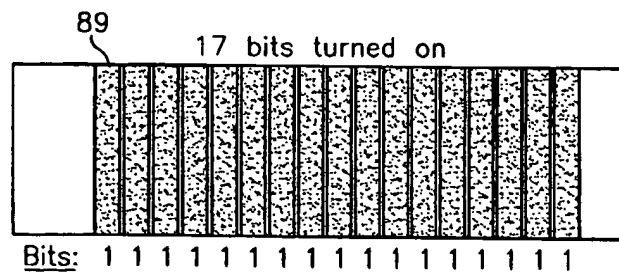
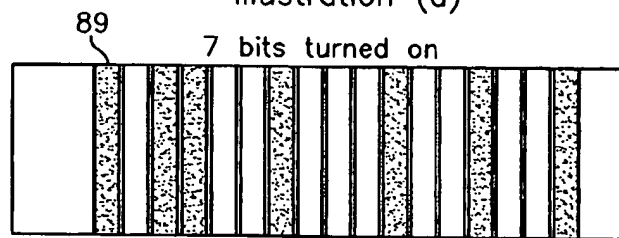


FIG. 29



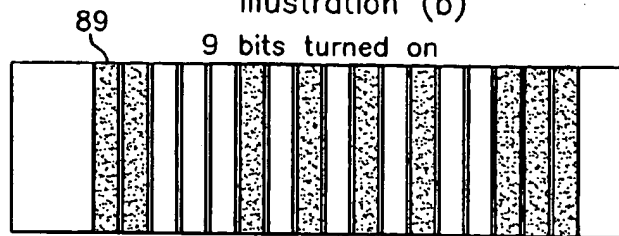
Bits: 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

Illustration (a)



Bits: 1 0 1 1 0 0 1 0 0 0 1 0 0 1 0 0 1

Illustration (b)

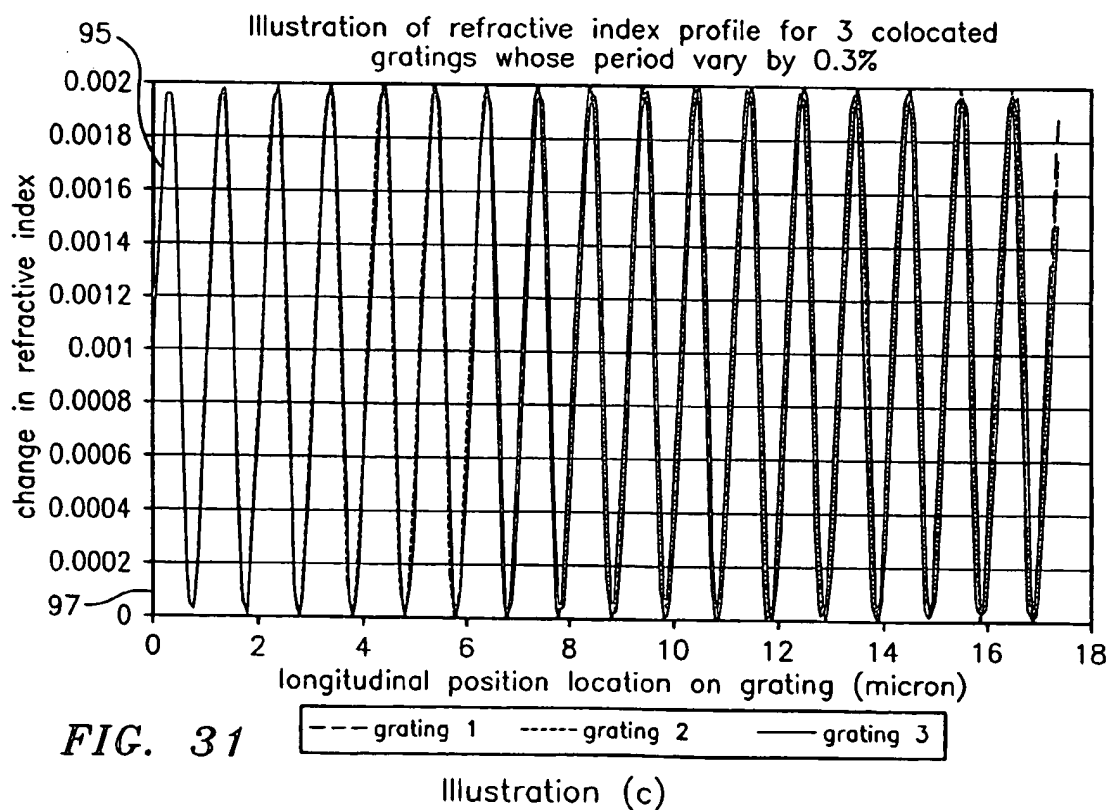
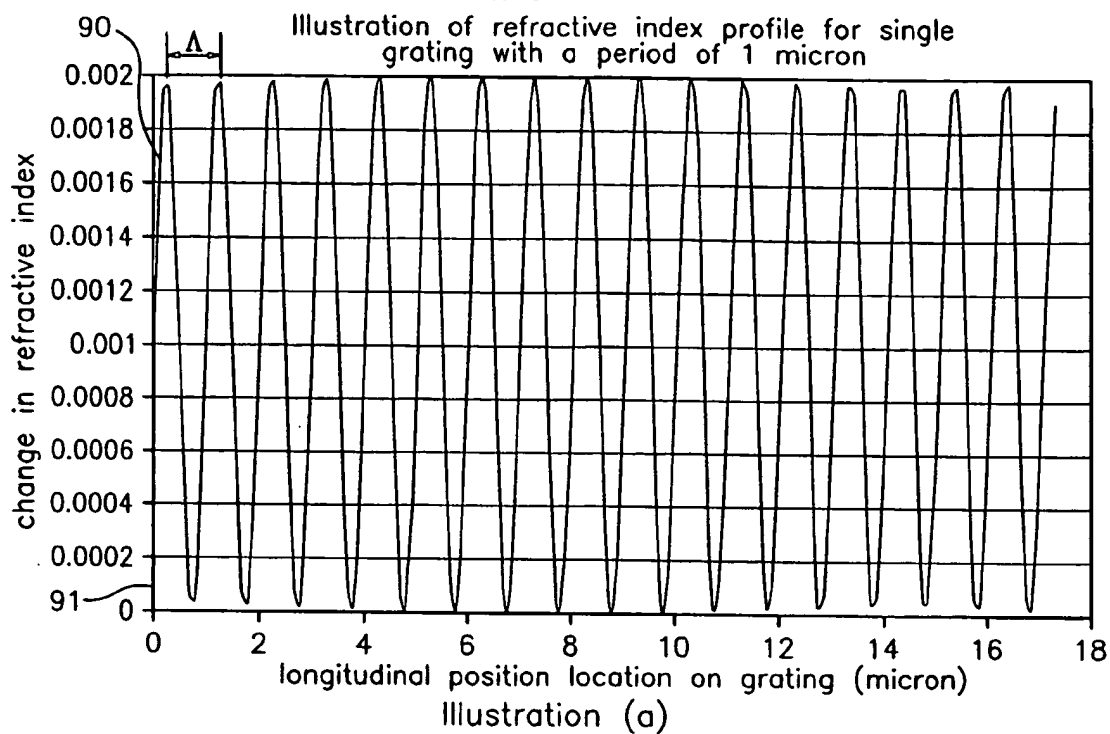


Bits: 1 1 0 0 0 1 0 1 0 1 0 1 0 0 1 1 1

Illustration (c)

FIG. 30

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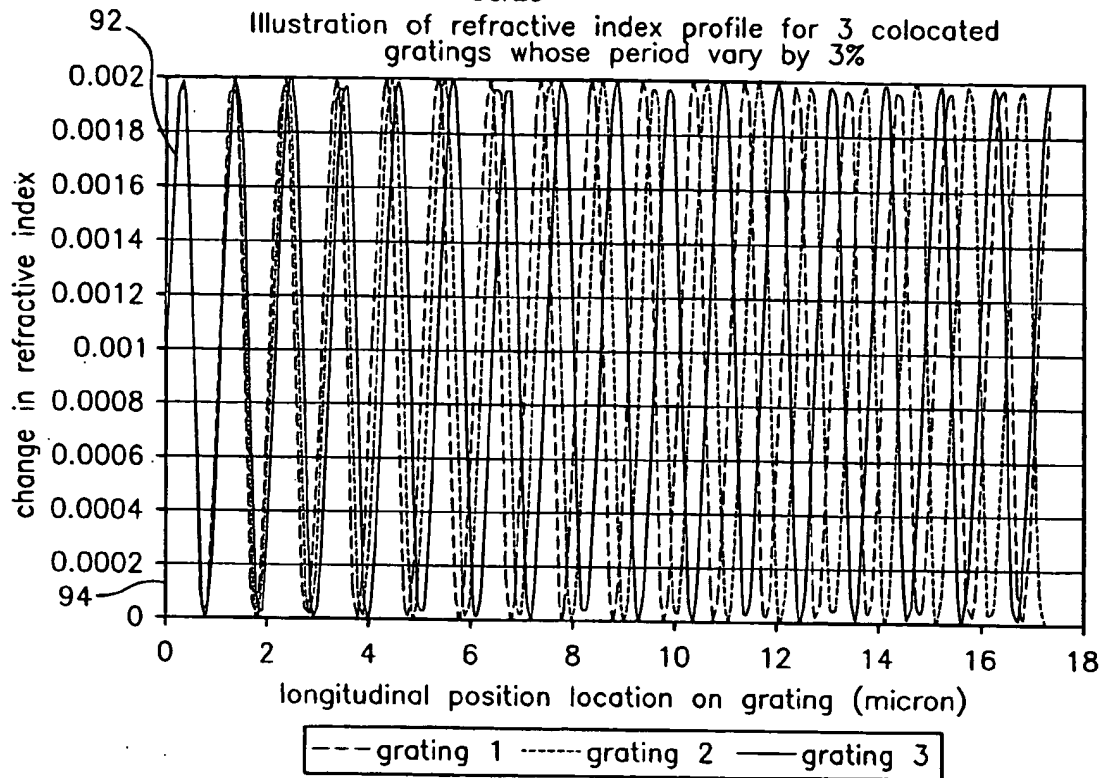


Illustration (b)

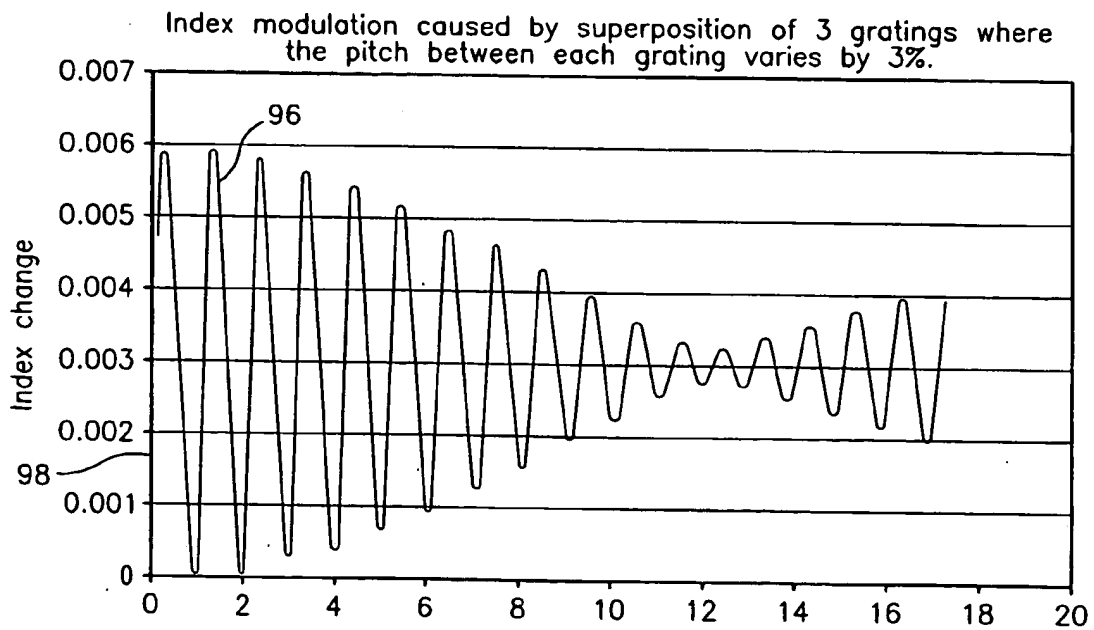
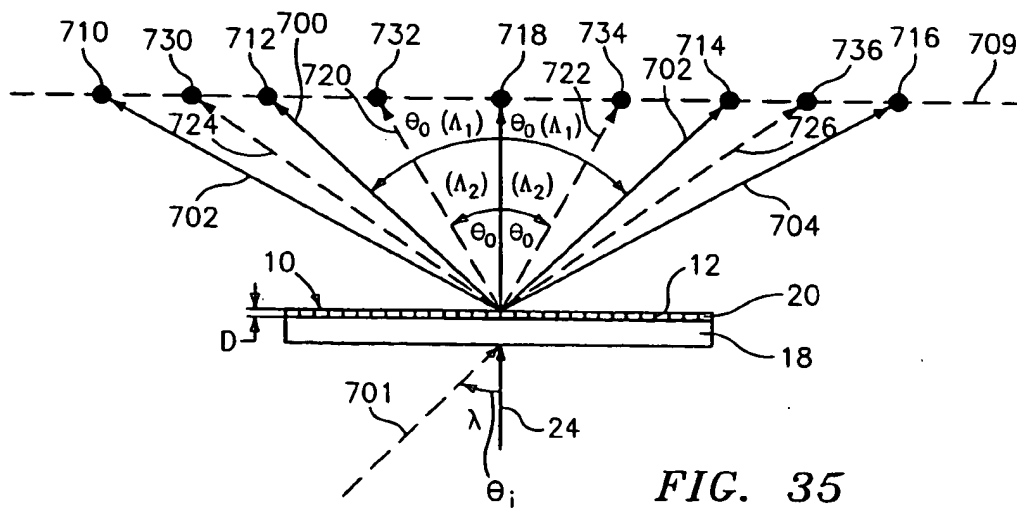
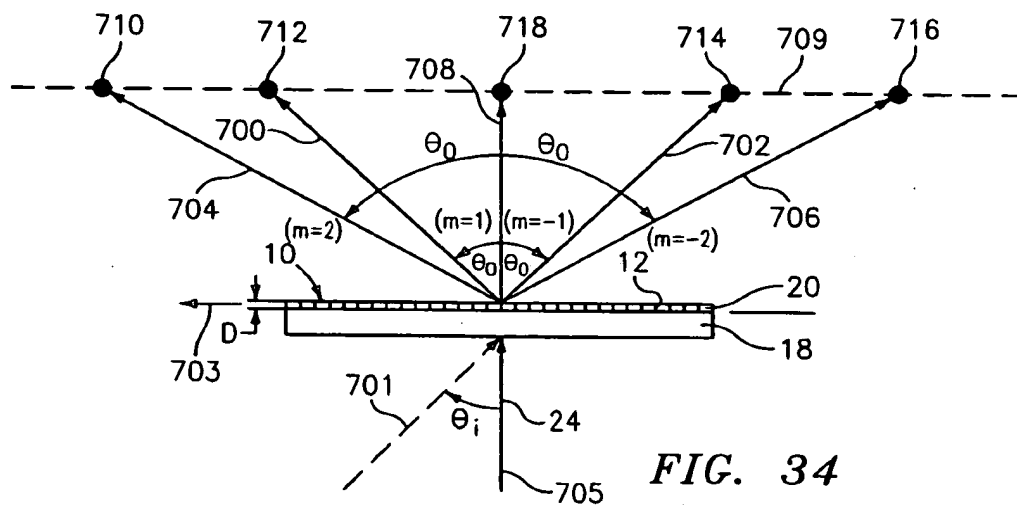
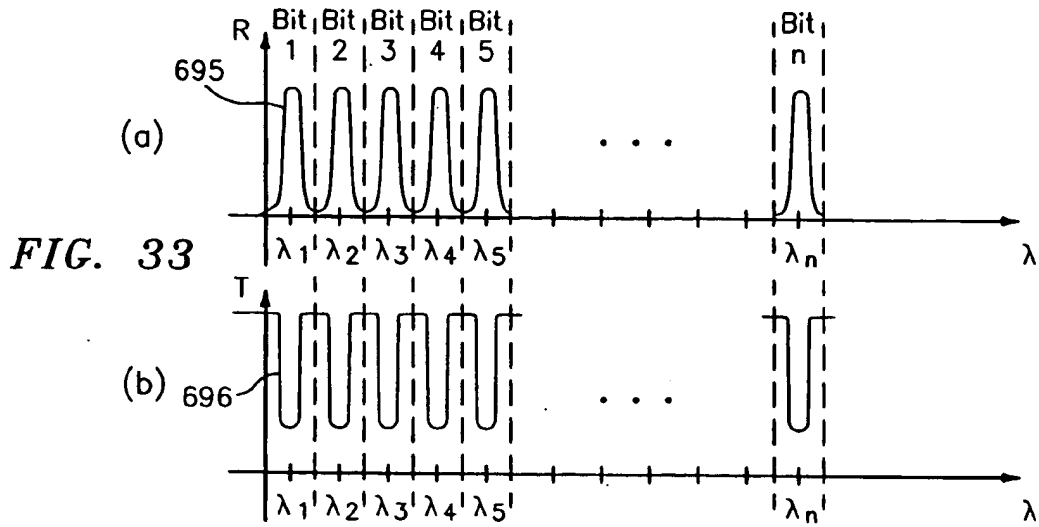


Illustration (d)

FIG. 31



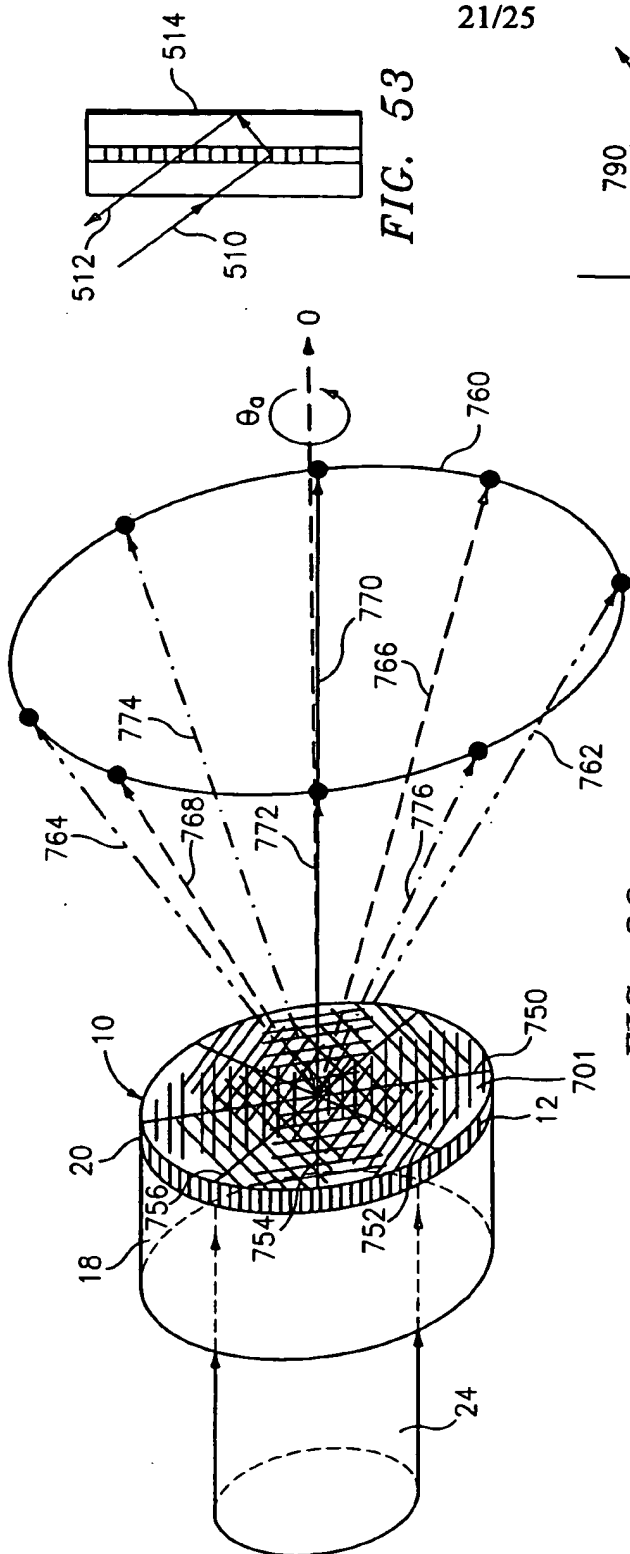


FIG. 53

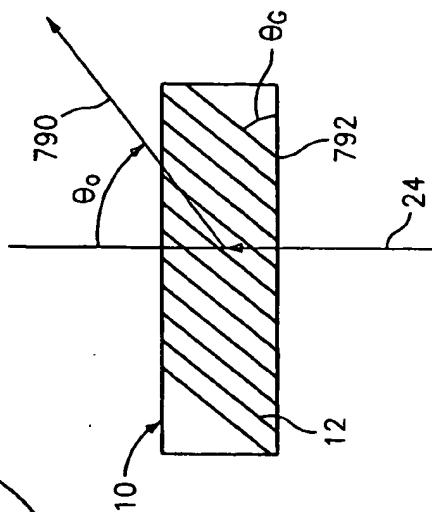


FIG. 37

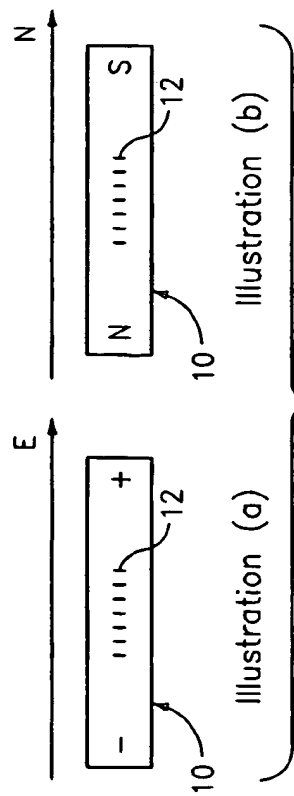


FIG. 54

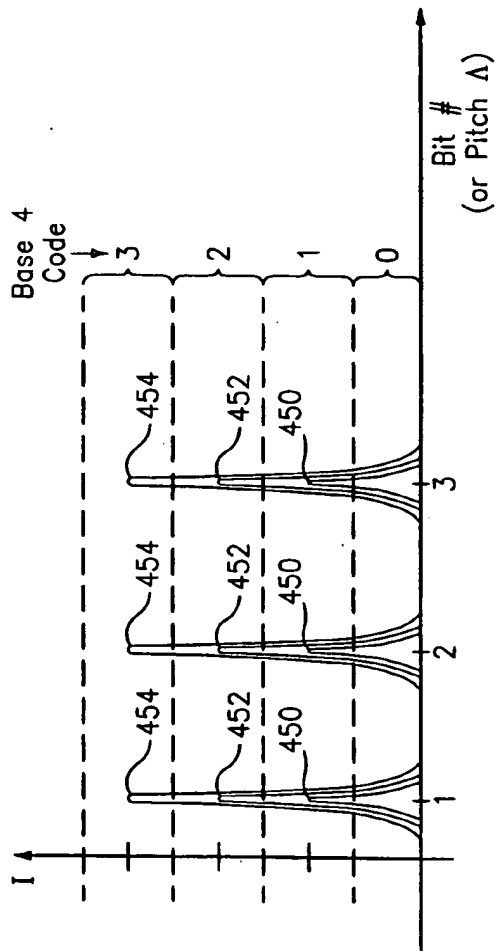


FIG. 38

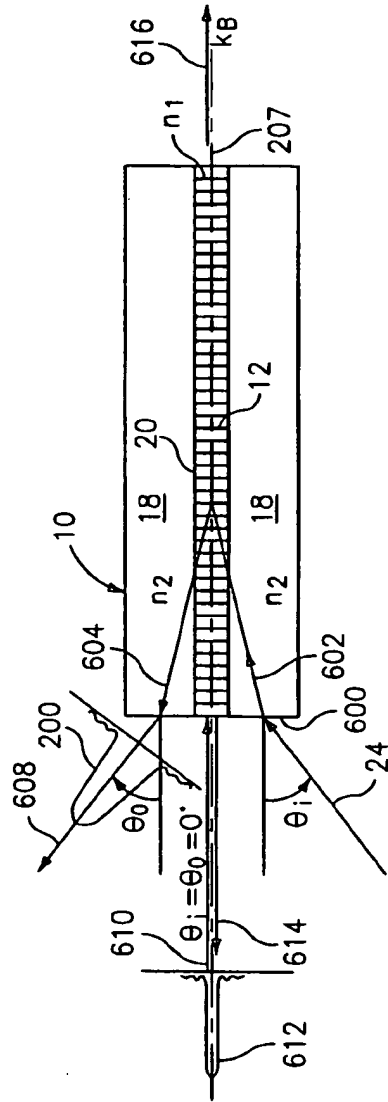


FIG. 39

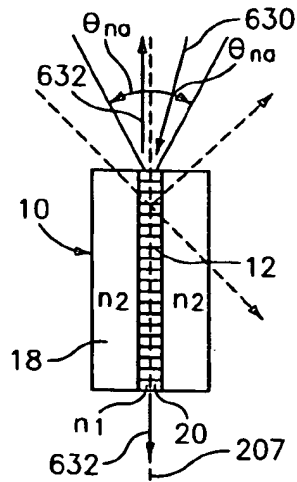


FIG. 40

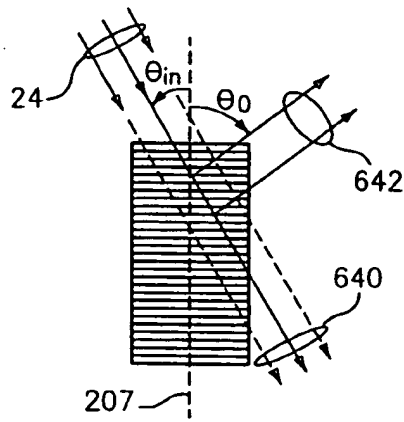
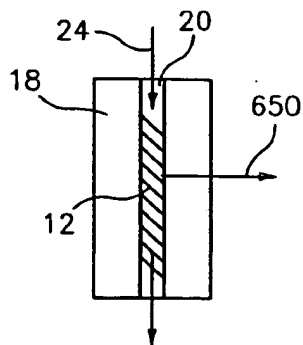
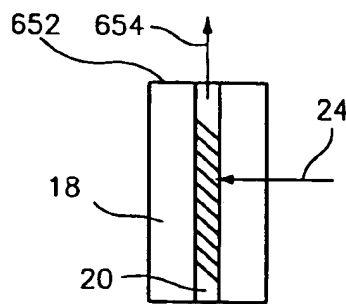


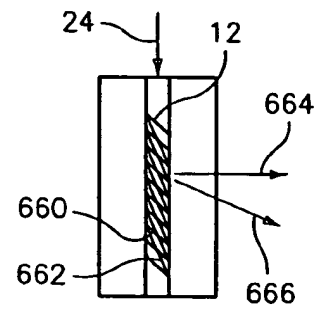
FIG. 41



Illustration(a)



Illustration(b)



Illustration(c)

FIG. 42

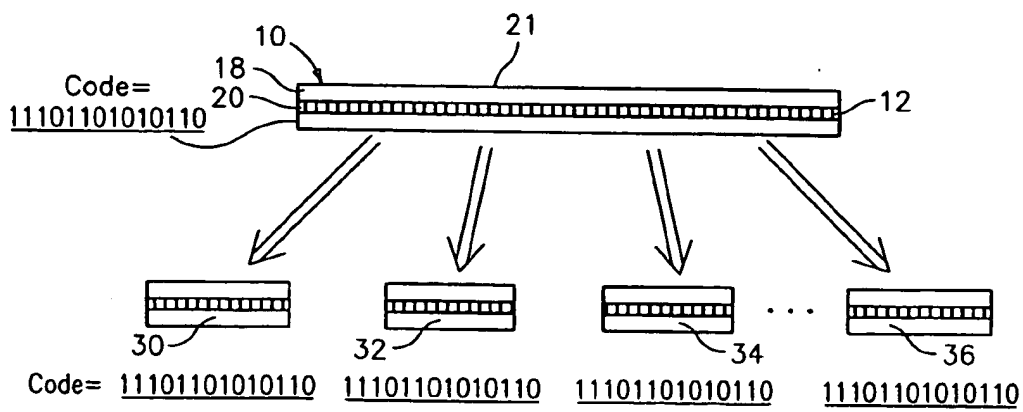


FIG. 44

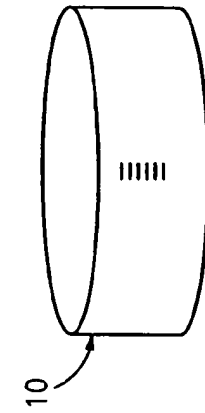
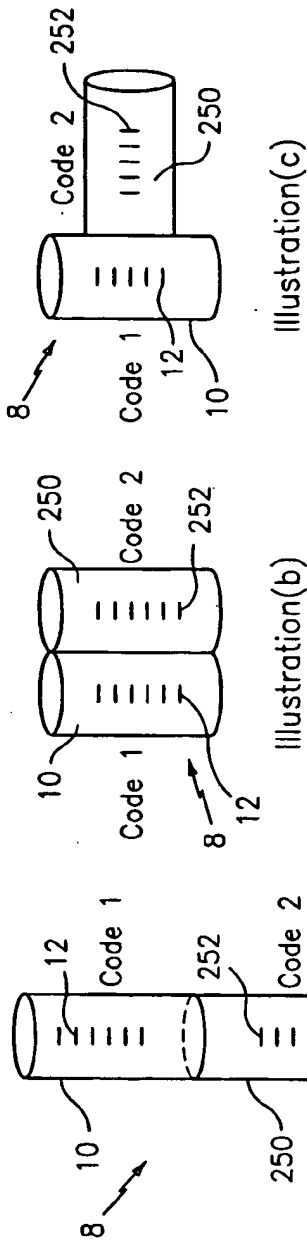


FIG. 48

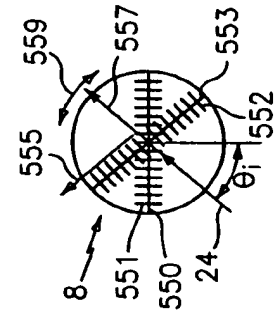


FIG. 43

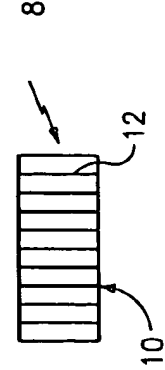


FIG. 45

FIG. 49

FIG. 47

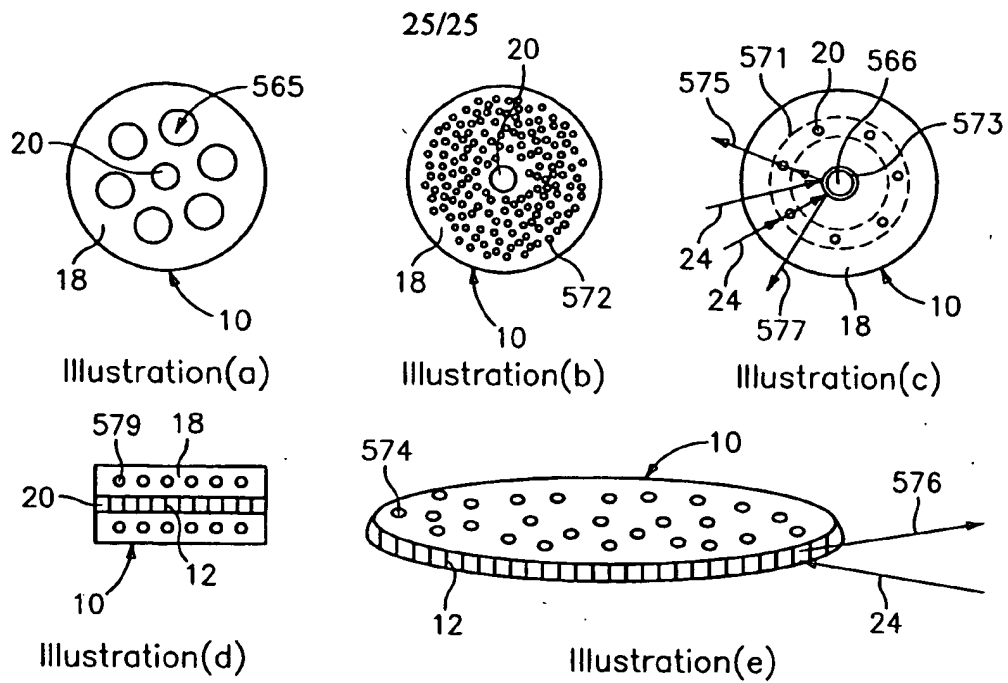


FIG. 50

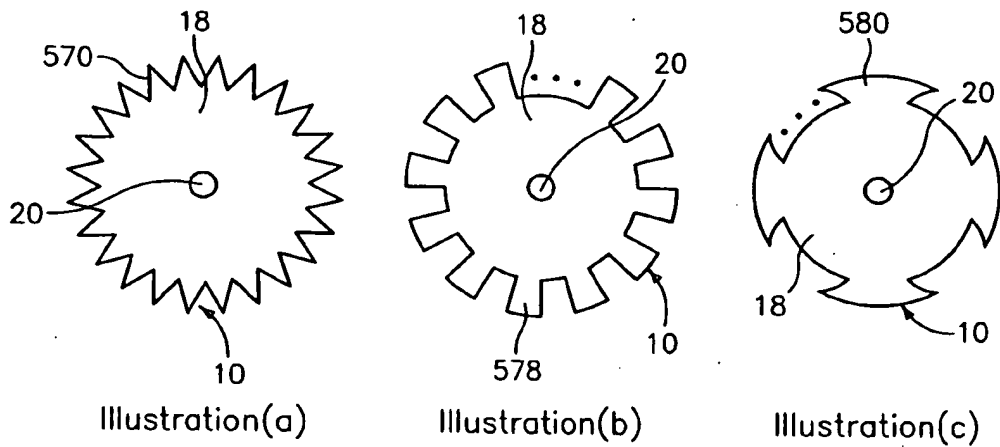


FIG. 51

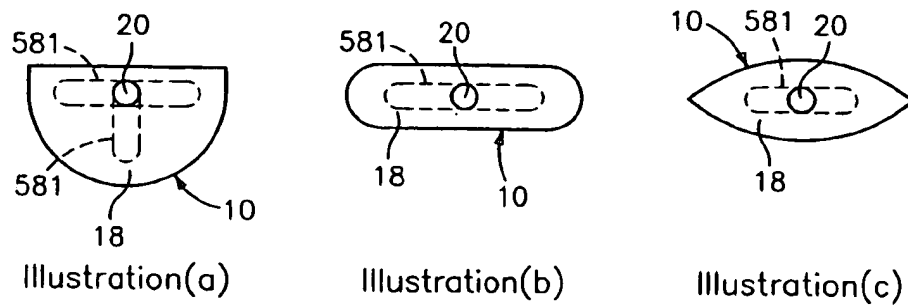


FIG. 52